

The diagram illustrates a network system architecture. A host system (10) is connected to a network system (12) via a network (14). The host system (10) includes a PC (20), host memory (38), a microprocessor/chipset (32), and storage media (40). The network system (12) includes a router (16), a PC (18), a network controller (42), a media connector (44), EEPROM (46), and flash ROM (48). A PCI bus (26) connects the host system to the network system.

Figure 1

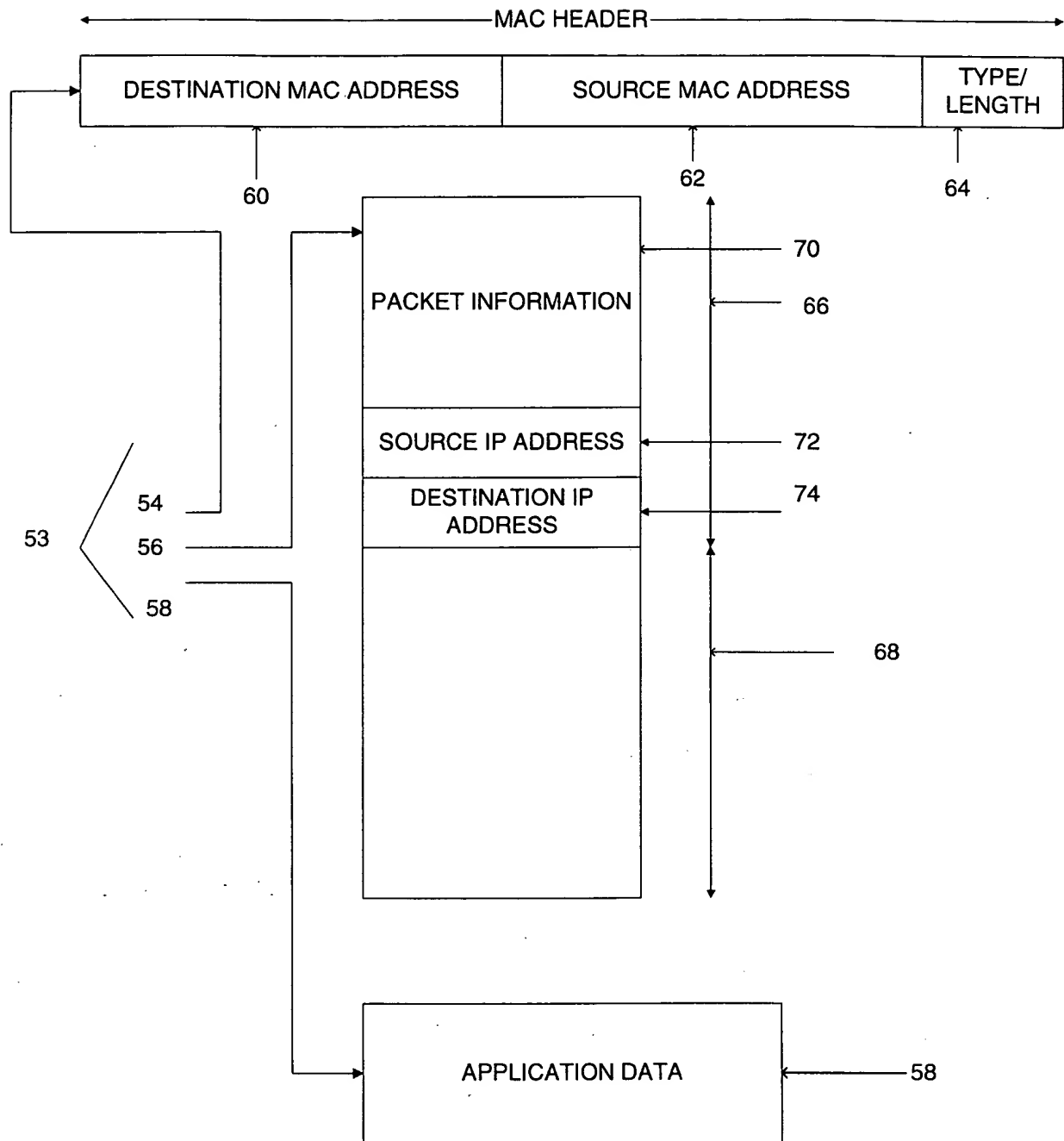


FIGURE 2

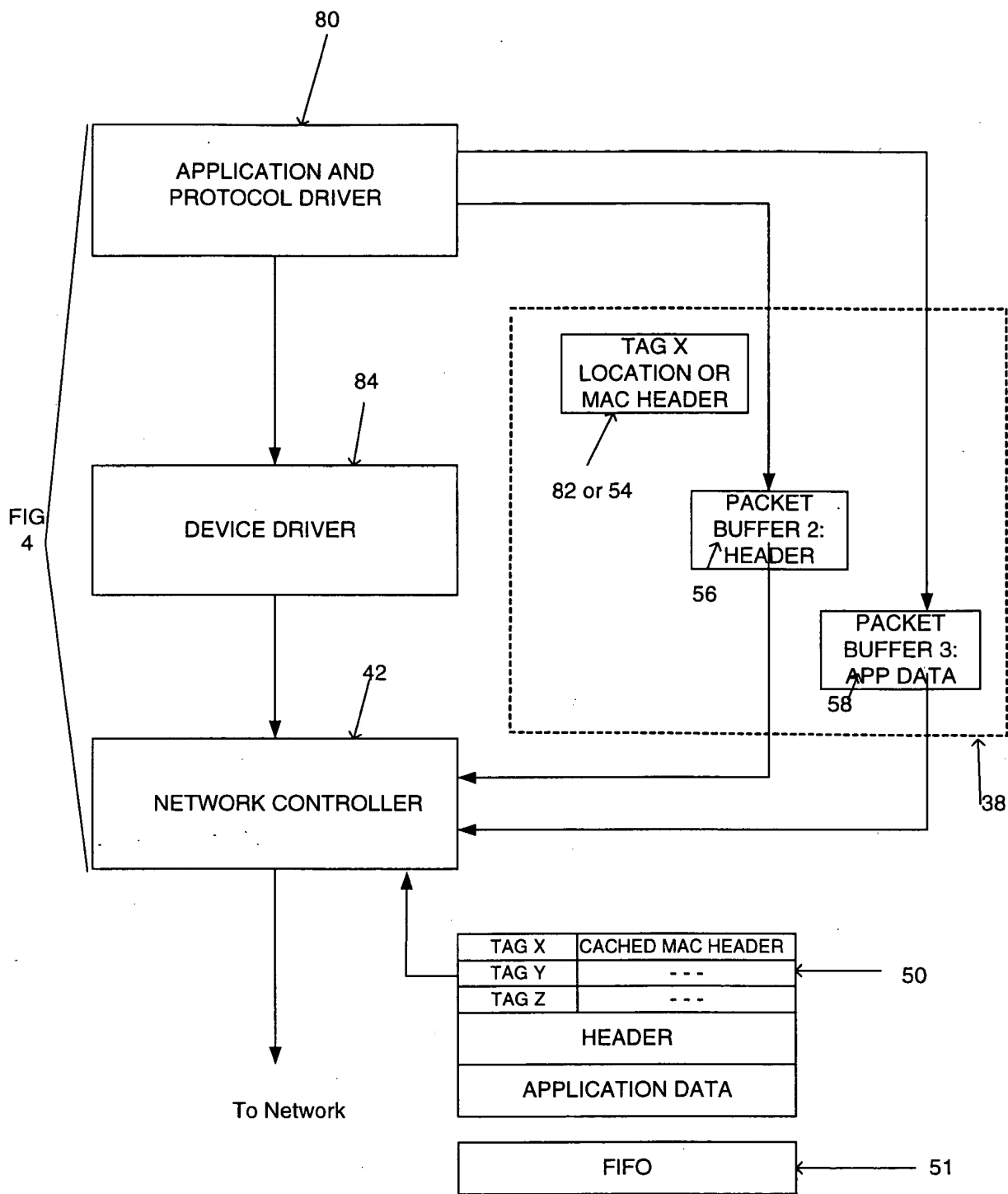


Figure 3

